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AF/2821
501.37426CX1

**EXPEDITED PROCEDURE PURSUANT
TO 37 CFR §1.116 REQUESTED**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): I. FUJII et al
Serial No.: 09/928,497
Filed: August 14, 2001
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
Group: 2827
Examiner: L. C. CRUZ

AMENDMENT AFTER FINAL

ATTN: BOX AF
Commissioner For Patents
Washington, D. C. 20231

March 18, 2003

Sir:

In response to the Final Office Action, dated December 18, 2002, the following amendments and remarks are respectfully submitted in connection with the above-identified application.

IN THE SPECIFICATION:

On Page 8, please replace the first full paragraph with that of the following:

Fig. 3 is a schematic device layout diagram showing one example illustrative of three cells which constitute the above-described circuit block.

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TECHNOLOGY CENTER 2827